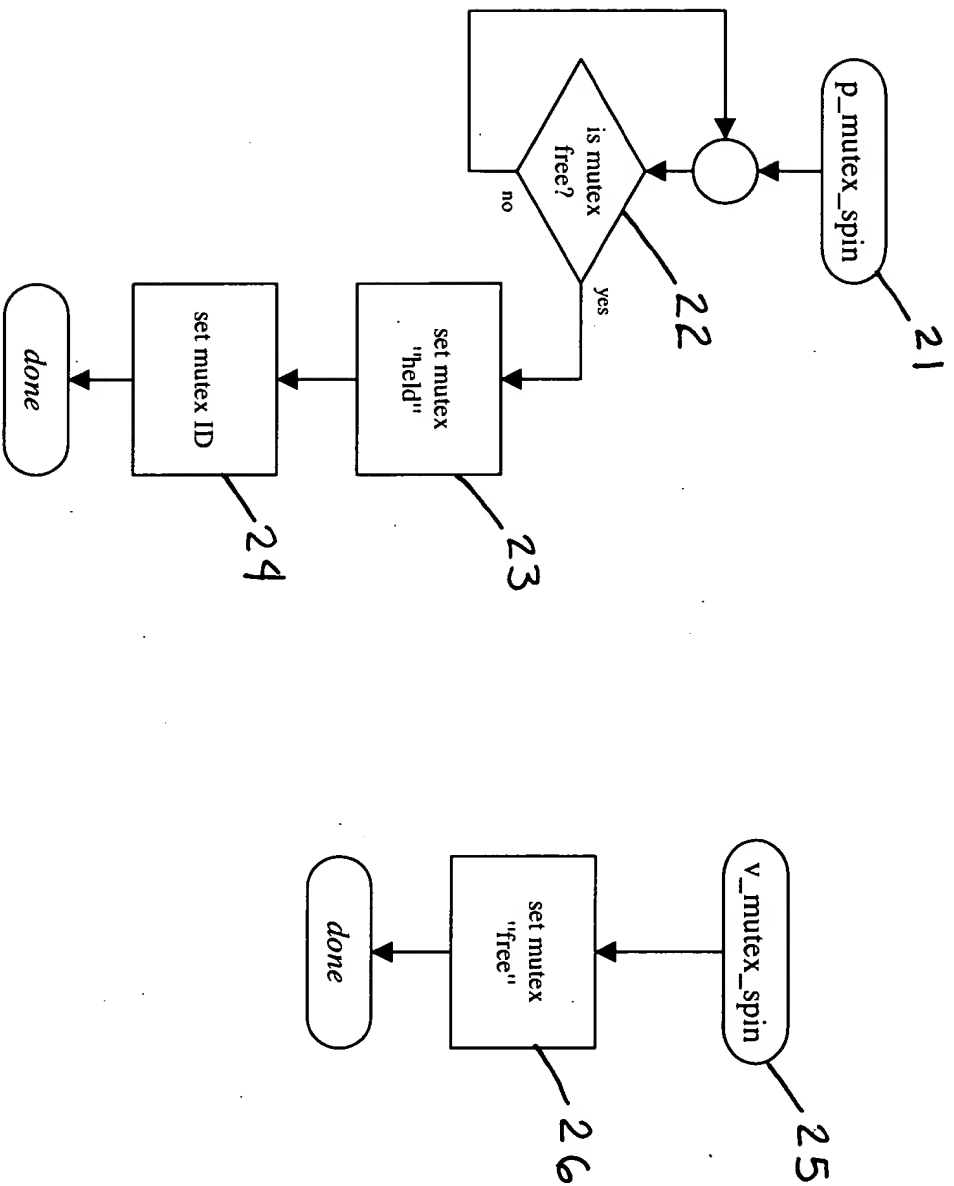


Fig. 1

FIG. 1 is a block diagram of a system architecture showing four Node/Computer units (10) interconnected in a mesh topology. Each unit (10) contains an OS Instance (13), a Panic Handler (11), and Mutex Primitives (12). The units are arranged in a 2x2 grid, and the connections are represented by lines that cross in the center of the diagram, indicating a full mesh interconnection between the four nodes.



2
Figure X: Spin Mutex

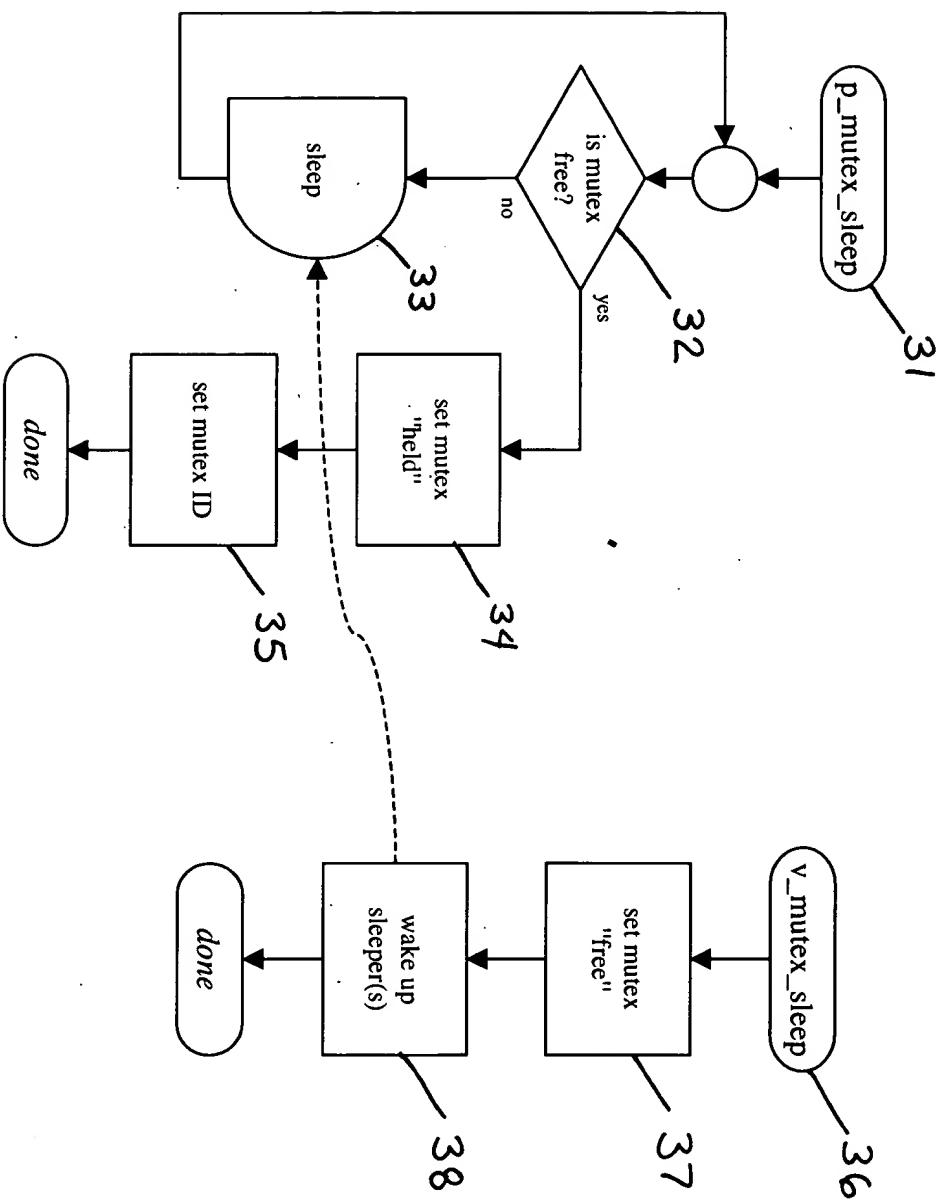
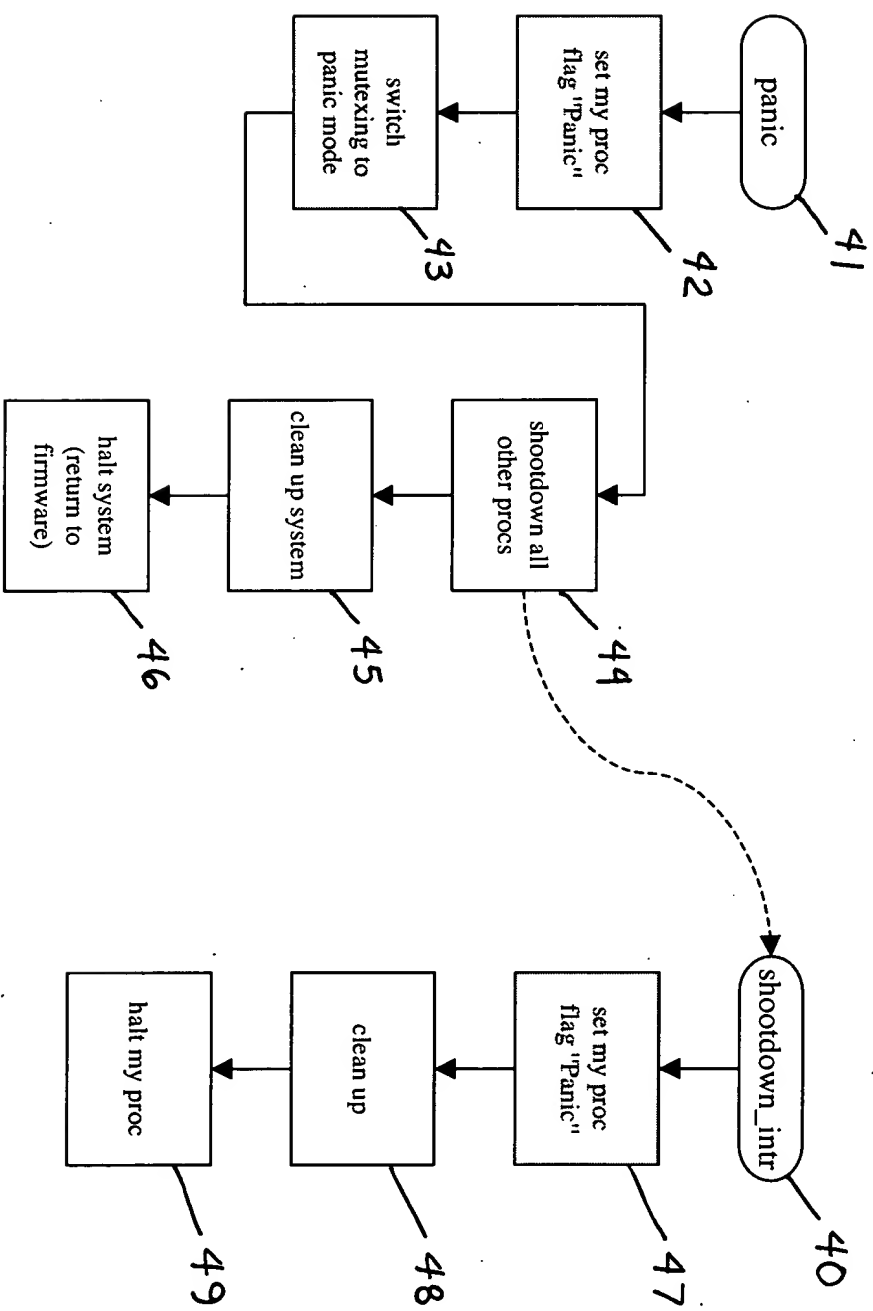
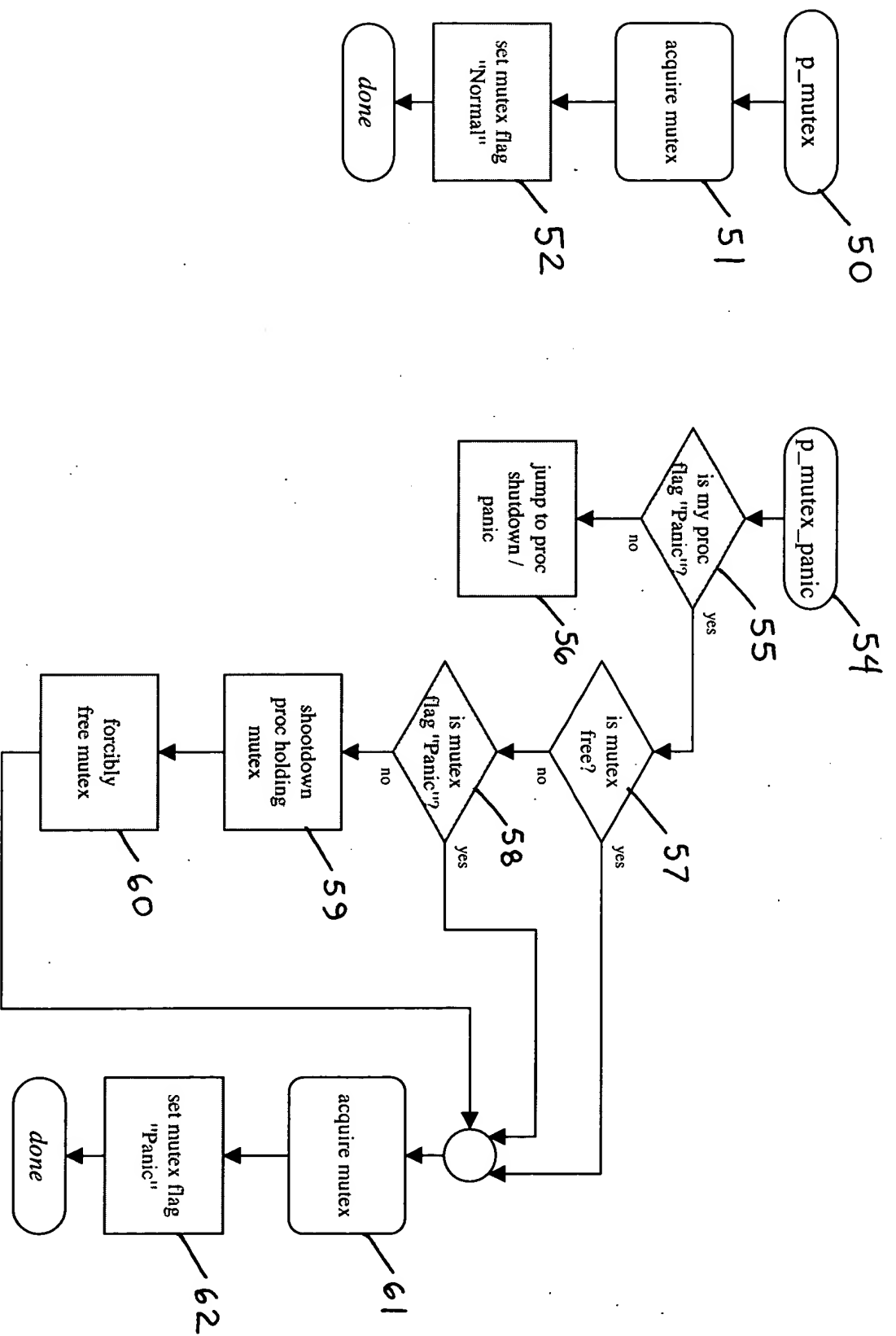


Figure 2a: Sleep Mutex



4
Figure 3: Panic Handling



5
Figure 4: Mutex Handling, Normal vs. Panic

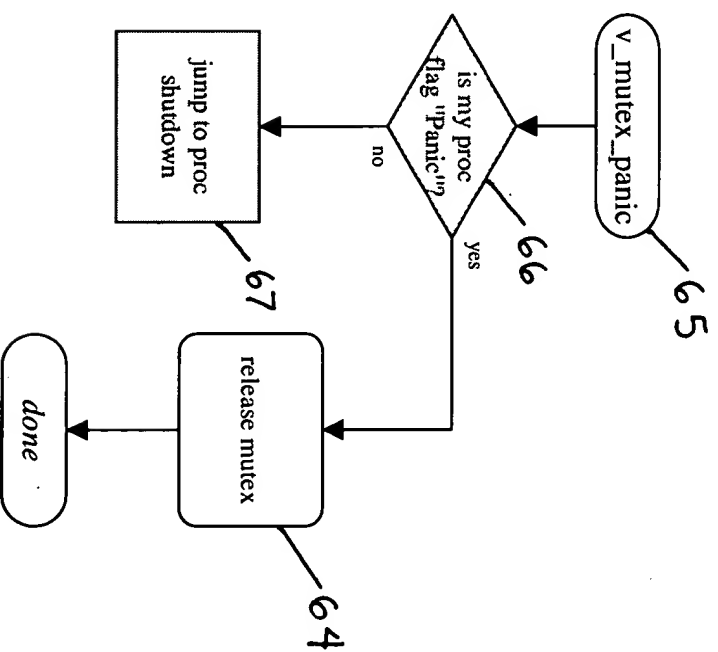
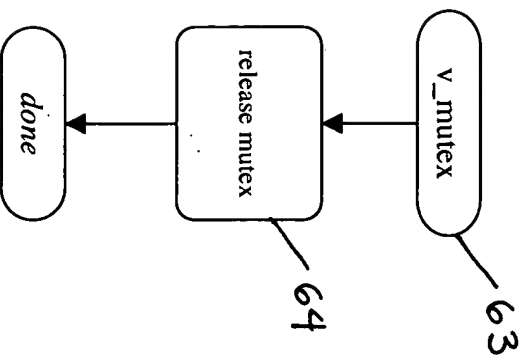


Figure 5: Mutex Handling, Normal vs. Panic (cont.)

1002

1008

1006

1004

1010

1012

This diagram shows a top view of a circular device. It features a central circular hole (1004) surrounded by a ring. Four rectangular segments (1006, 1008, 1010, 1012) are positioned around the central hole, each with a curved outer edge. A label 1002 points to the outer boundary of the device.

FIG. 7

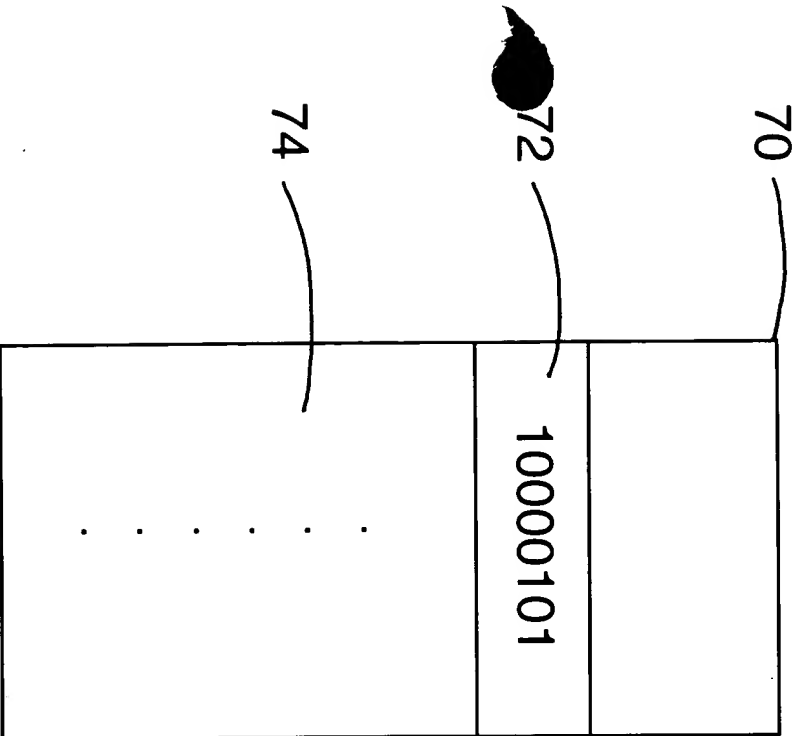


Fig. 8A
Pre-panic mutex

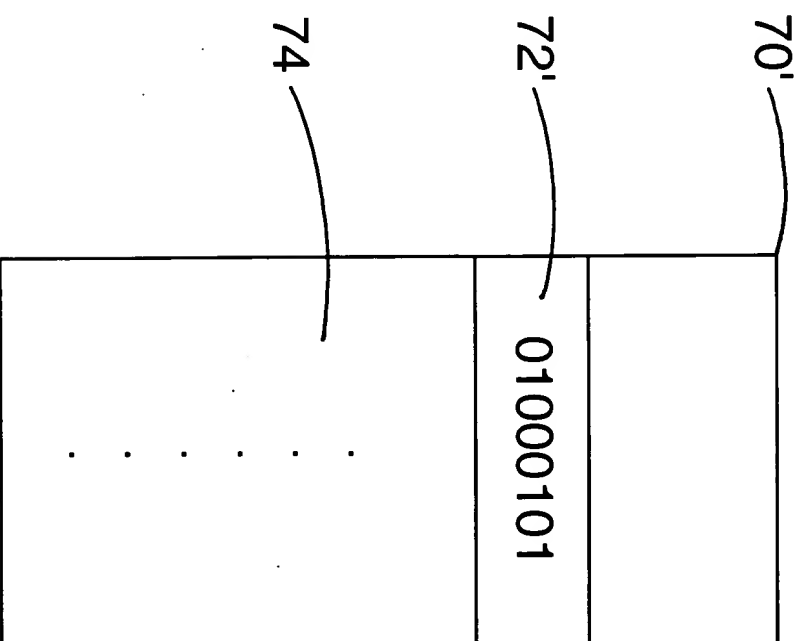


Fig. 8B
Post-panic mutex